

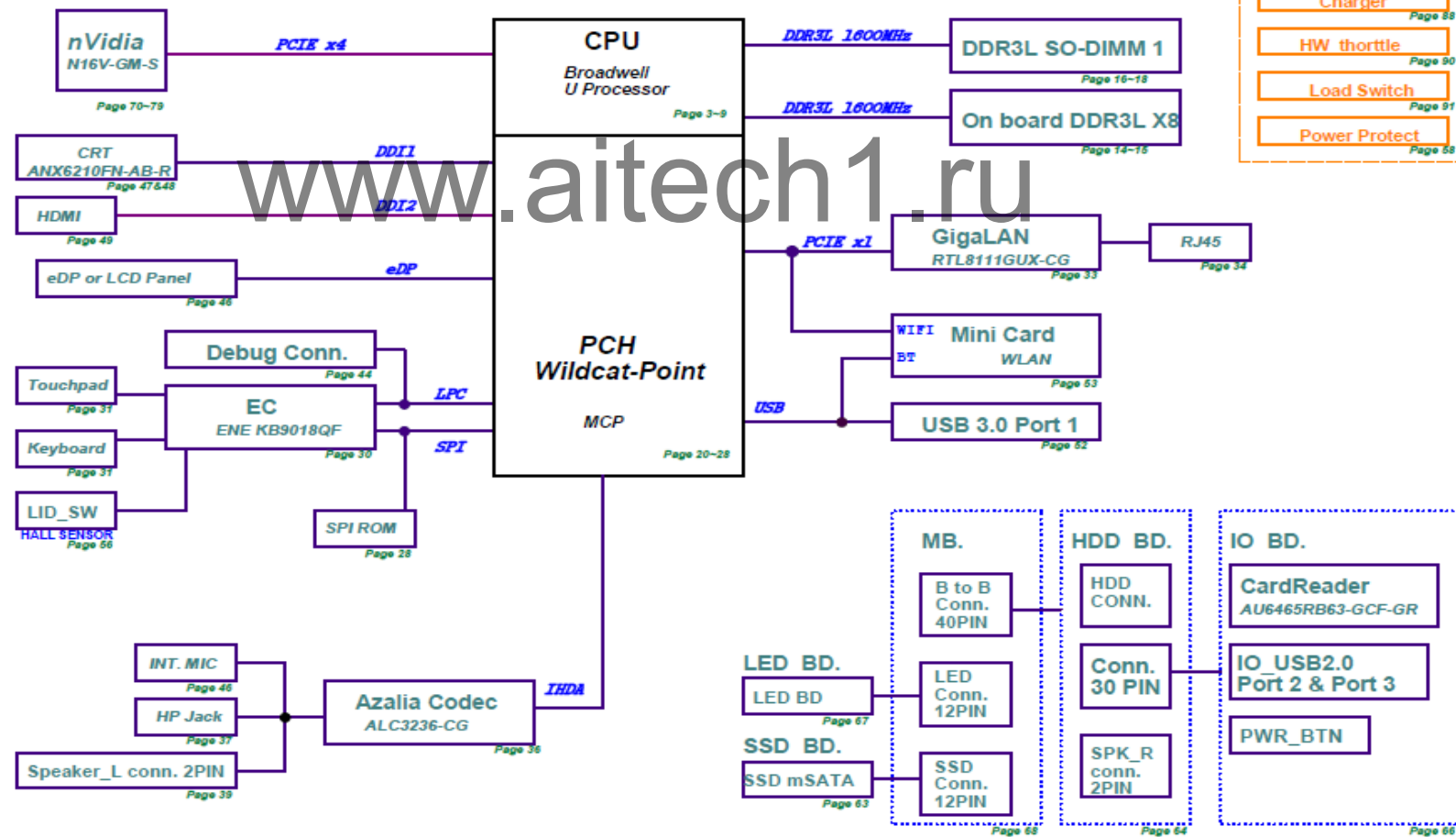
BLOCK DIAGRAM

BLOCK DIAGRAM

X302 LA/LJ SCHEMATIC

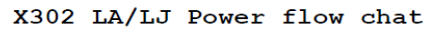
(LA : UMA)

(LJ : DGPU=Nvidia N16V-GM-S(GT920M))

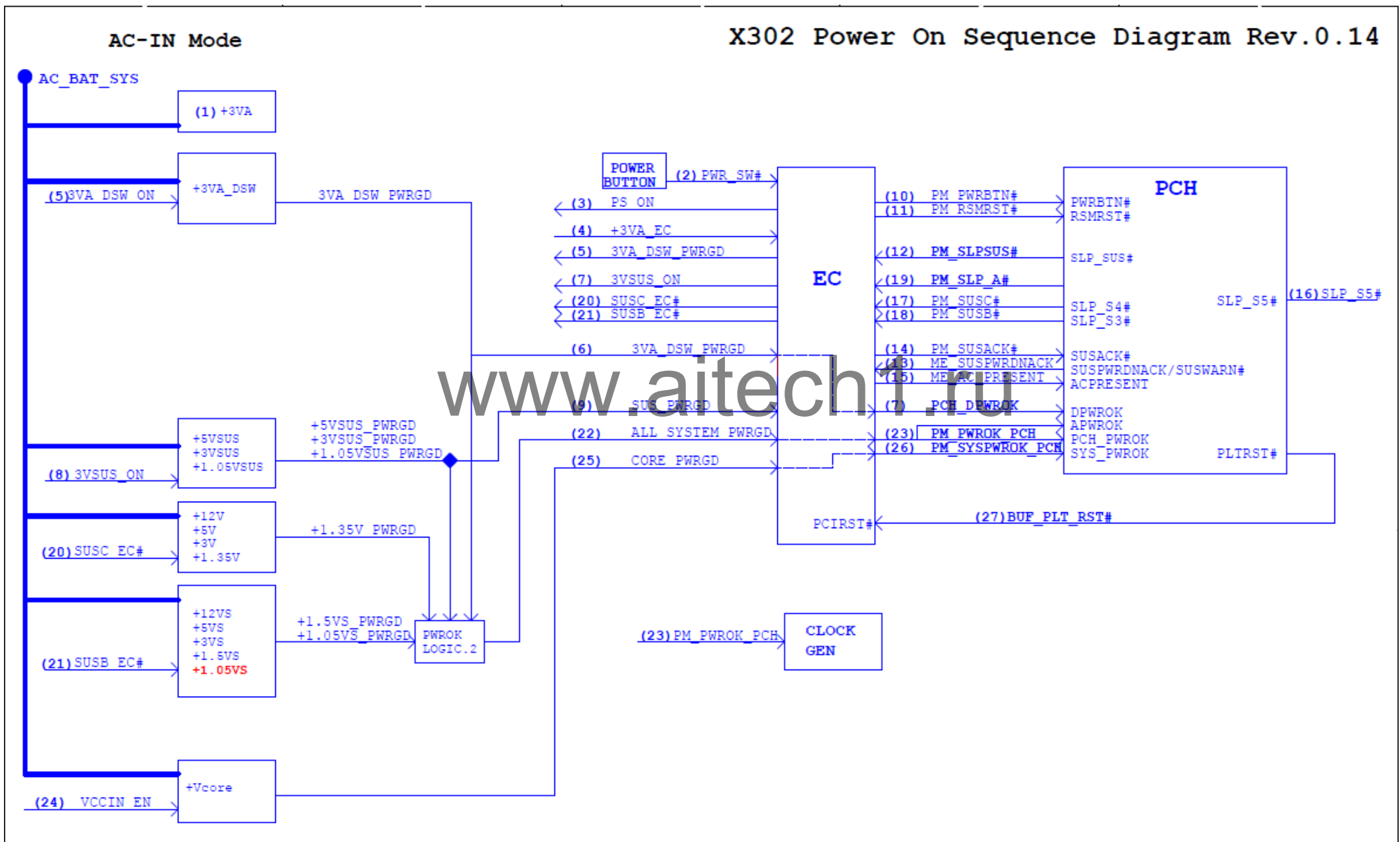


VCORE & VGFX	Page 80
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+1.05VSUS	Page 82
DDR +1.35V&+0.675VS	Page 83
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+VGA VCORE	Page 85
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POWER FLOW



POWER ON SEQUENCE



AC POWER ON SEQUENCE

AC-IN Mode

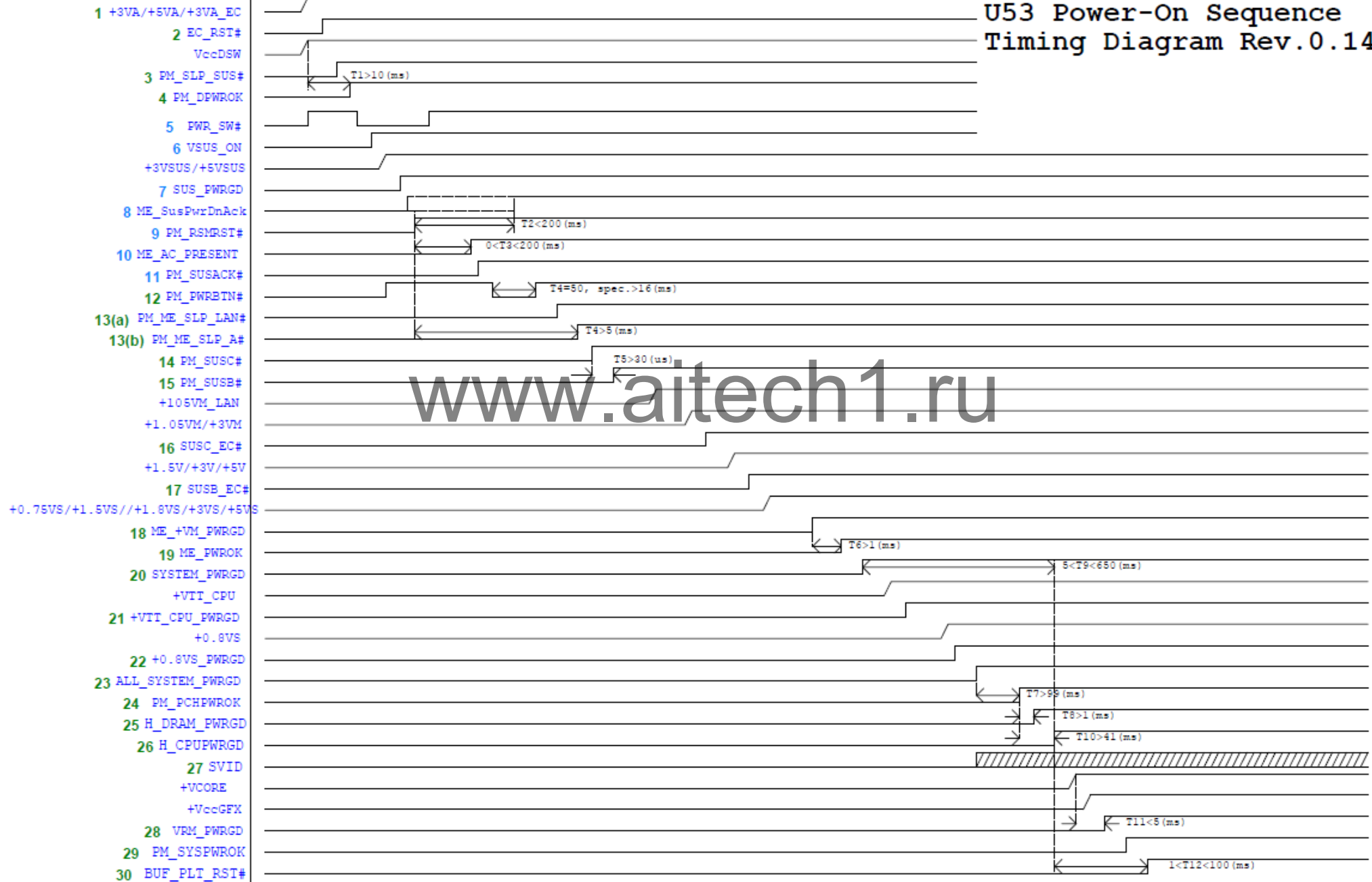
U53 Power-On Sequence Timing Diagram Rev.0.14



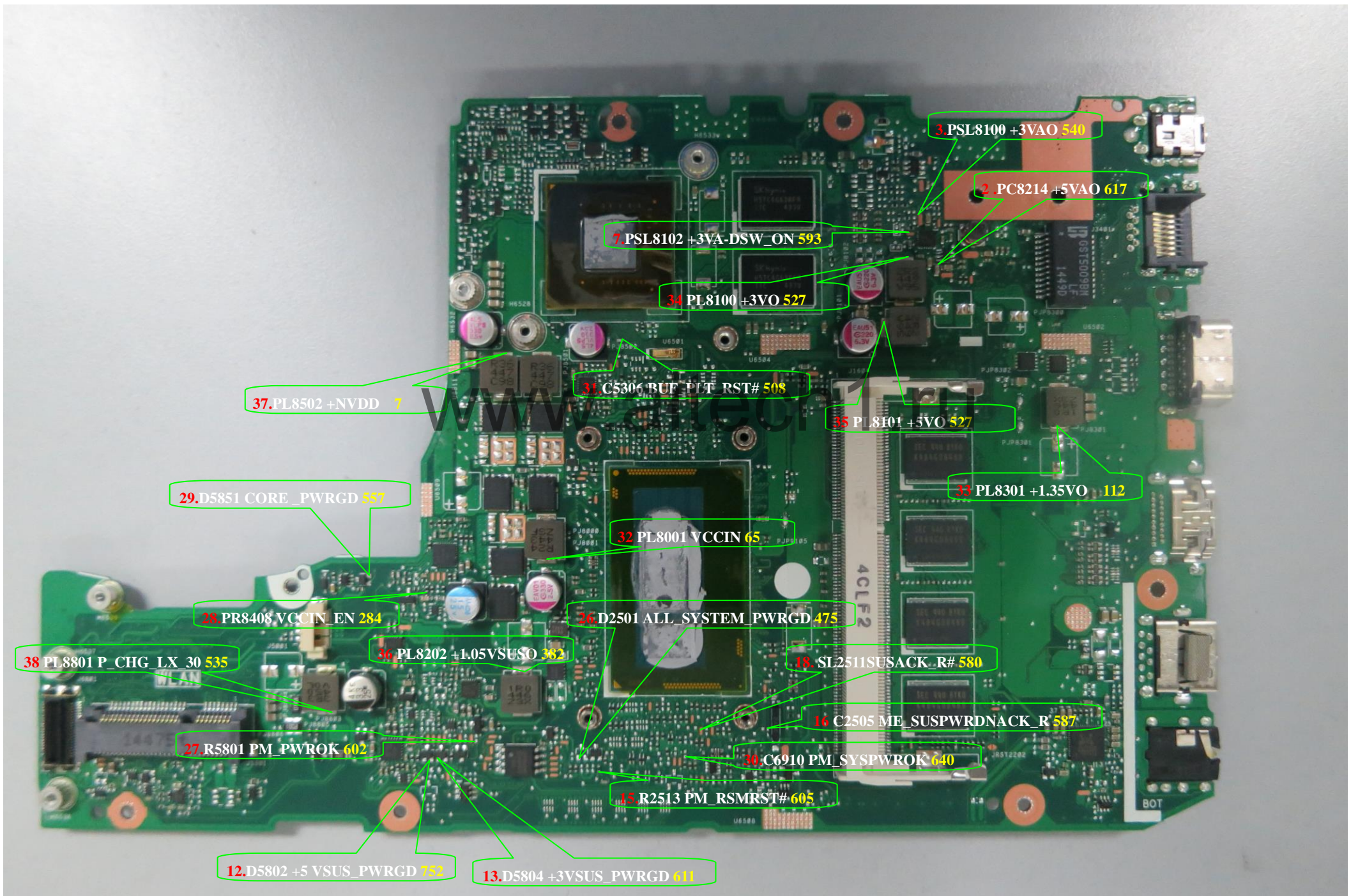
DC POWER ON SEQUENCE

DC-IN Mode

U53 Power-On Sequence
Timing Diagram Rev.0.14



Signal Measure Point-Bottom



Signal Measure Point-Top

